

# TEJASVINI SINGH

stejasvini996@gmail.com | +91-6389702514 |

## EDUCATION

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<b>Rajiv Gandhi Institute of Petroleum Technology, Jais, Uttar Pradesh</b>	2022-2026
Bachelor of Technology in Electronics Engineering	CPI-8.66
<b>City Montessori School, Kanpur Road, Lucknow</b>	2022
Class 12, ISC Board	Percentage-95.2
<b>City Montessori School, Rajajipuram, Lucknow</b>	2020
Class 10, ICSE Board	Percentage-96

## PROJECTS

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### Designing an FIR Filter using Machine Learning (ongoing)

- Exploring [specific algorithms, e.g., neural networks, support vector machines, etc.] to enhance filter efficiency for [specific applications, e.g., signal processing, communications].
- Tools Used: Python, Matlab

## SKILLS

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<b>Software Programming Languages</b>	C++, C, Java, Python(openCV, Numpy, Pandas, Matplotlib), Matlab
<b>Hardware Description Languages</b>	Verilog, System Verilog, VHDL
<b>EDA Tools</b>	Xilinx Vivado, Xilinx Vitis, Xilinx SDK, Questasim
<b>Softwares</b>	Autocad

## RELEVANT COURSES

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Analog Circuit and System, Control System, Digital Circuit and System, Signal and Systems, Solid State Electronic Devices, Material Engineering, Statistical Methods and Data Analysis

## POSITION OF RESPONSIBILITIES

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### Teaching Volunteer

Gyanarpan, Project Amethi

## ACCOMPLISHMENTS

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### Flipkart Runway Program | [2024]

- Participated in Flipkart's exclusive program designed to mentor and empower individuals in the [e-commerce/technology] domain.

### IPR Event | [2024]

- Engaged in discussions and workshops related to intellectual property laws and patenting strategies.

## LANGUAGES

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- Hindi
- English